

Electrical Characterization of InGaN/GaN Multiple-Quantum-Well Structures by Thermal Admittance Spectroscopy

N. D. NGUYEN (a), M. SCHMEITS (a), M. GERMAIN (a), B. SCHINELLER (b), and M. HEUKEN (b, c)

(a) *Institut de Physique, Université de Liège B5, 4000 Liège, Belgium*

(b) *ALXTRON AG, Kackertstr. 15-17, 52072 Aachen, Germany*

(c) *Institut für Halbleitertechnik, RWTH Aachen, Templergraben 55, 52056 Aachen, Germany*

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Abstract

Experimental results of electrical characterization of InGaN/GaN multiple-quantum-well electroluminescence test structures obtained by thermal admittance spectroscopy are presented. The studied GaN:Mg/ $5 \times$ (InGaN/GaN)/GaN:Si structures were grown on *c*-plane sapphire substrate by metal-organic vapor phase epitaxy. Admittance measurements were conducted from room temperature down to 125 K for a wide frequency range and for different applied bias voltages. Analysis of the capacitance versus frequency curves shows the presence of several cutoff frequencies which originate from the response of equivalent *RC* series circuits and give peaks in the conductance divided by angular frequency. The dependence of the position and the amplitude of these peaks on temperature is discussed.

1. Introduction

Nitride semiconductors have attracted much attention during the past decade as they are used for the fabrication of short-wavelength light-emitting diodes (LEDs) [1] and laser diodes [2]. The InGaN alloy has become the major building block of these devices due to the tunability of its energy gap which covers the visible to near ultraviolet light spectra. It is used in quantum structures such as InGaN/GaN multiple-quantum-well (MQW) emitters. Admittance measurements of these structures are of importance as they contribute to the understanding of current transport in these devices. Such studies have been performed previously on QW systems, but mostly on GaAs or Si-based structures [3, 4].

2. Experiment

The studied electroluminescence test (ELT) structure was grown by metalorganic chemical vapor deposition (MOCVD) on sapphire substrate. An approximately 25 nm thick GaN nucleation layer was first deposited at low temperature, followed by a 0.9 μm thick unintentionally doped GaN layer and a 1.2 μm thick Si-doped ($3 \times 10^{18} \text{ cm}^{-3}$) GaN layer. The active region consists of a fivefold InGaN/GaN:Si (2 nm/7 nm) multiquantum well, on top of which a 100 nm thick GaN:Mg layer was deposited. The admittance of the structure was measured with a Hewlett-Packard 4192A LF admittance analyzer for frequencies in the range of 100 Hz to 3 MHz and for bias voltages from -3 to +3 V. The temperature of the sample was varied from 125 K up to room temperature (RT, 293 K).

3. Results and Discussion

In Fig. 1 the thermal admittance spectroscopy (TAS) data obtained for the ELT structure are presented. The frequency responses of the capacitance C and the conductance G divided by the angular frequency $\omega = 2\pi f$, where f is the measurement frequency, are shown for temperatures T ranging from 275 to 125 K and zero applied voltage. At the highest temperatures, the capacitance exhibits a plateau regime for frequencies up to 30 kHz followed by a transition region where the capacitance decreases down to a high frequency value which is dependent on the temperature. For $T < 170$ K, the plateau regime is reduced below 1 kHz and could not be observed due to the limitation of the resolution of the measurements at low frequency. As the temperature is lowered, the transition region apparently widens and is shifted towards lower frequencies, while a variation of the slope can be observed between 100 kHz and 1 MHz for the lowest temperatures. The large peak centered at 250 kHz in the G/ω curve for $T = 275$ K corresponds to the mean cutoff frequency in the capacitance curve. When the temperature is decreased, the position of this peak is shifted towards the low frequencies while its amplitude is reduced. For temperatures below 230 K, we can observe a shoulder which arises from the high frequency region and resolves itself as a second structure for $T < 125$ K.

The presence of a peak in the G/ω curves suggests that the frequency response of the MQW structure to an ac voltage excitation is similar to that of an electrical RC series circuit, with a peak frequency $f_c = 1/(2\pi RC)$ corresponding to a cutoff in the capacitance curve. However, in this case, the width of this peak cannot be related to a single RC circuit. Indeed the full width at half magnitude (FWHM) of the G/ω peak corresponding to the ac response of a single RC series circuit has a value of $\Delta(\log(2\pi f)) = \log[(2 + \sqrt{3})/(2 - \sqrt{3})] \approx 1.14$ which is independent on the values of the resistance R and the capacitance C . The estimated FWHM of the observed peak is larger than 1.14, it therefore cannot correspond to a single equivalent RC series circuit. An optimized fit procedure has been used to determine the composition of the equivalent circuit which would reproduce the various experimental admittance curves. The resistive and capacitive elements contained in the fit model are temperature and voltage-dependent. In this work, we focus on the temperature dependence of the various R_j and C_j components. In Fig. 2a we show the fitting of G/ω at room temperature by the equivalent electrical circuit [5] represented as inset. The RC series circuits simulate the ac behavior of the quantum wells while the resistance R_p could represent the effect of a recombination current path or a leakage current, and C_p the depletion capacitance. The values of R_j and C_j are generally functions of the temperature, yielding a dependence of each cutoff frequency $f_{c,j} = 1/(2\pi R_j C_j)$ on the temperature. Results of the fitting are shown in Fig. 2b for temperatures between 215 and 275 K. For $T < 245$ K, the RC component with the lowest cutoff frequency yields no contribution, reducing the model to four RC series circuits in parallel with R_p and C_p .

The equivalent electrical circuit model used for the fitting of G/ω also reproduces the measured impedance of the ELT structure. In Fig. 3, the obtained curves of the real and imaginary parts of the impedance Z are shown with the capacitance C and G/ω curves. The cutoff frequency $f_{c,s} = 1/(2\pi R_s C_p)$, where R_s is the series resistance, would appear at higher frequencies, leading to an additional peak in G/ω curves. But its position would fall outside the measurement range. It was therefore not included in this analysis.

Activation energies related to defect levels can be obtained by a classical interpretation of TAS data [6, 7]. Although one should be careful when using such a method [8], this type of analysis has been applied to QW structures, either to assess the presence of defects [9] or to determine QW parameters [10]. The Arrhenius plot of the cutoff frequencies corresponding to the five RC series circuits, shown in Fig. 4, gives values of the activation energies ranging from 32 to 248 meV with an accuracy of about 10%. The observed cutoff frequencies $f_{c,j}$ could therefore be related to a thermionic activation mechanism of the QWs [4]. The obtained values of the activation energies cover a large range, which may be an indication of the existence of discrepancies in the composition of the InGaN alloy or in geometrical parameters of the QWs. Further investigations, based on the solution of the basic semiconductor equations [8] for the ELT structure, are being performed in order to clarify the microscopic origin of the activation energies.

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Fig. 1. Experimental curves of a) capacitance C and b) conductance G divided by the angular frequency ω , for zero applied voltage. The temperature was varied from 275 down to 125 K by steps of -15 K

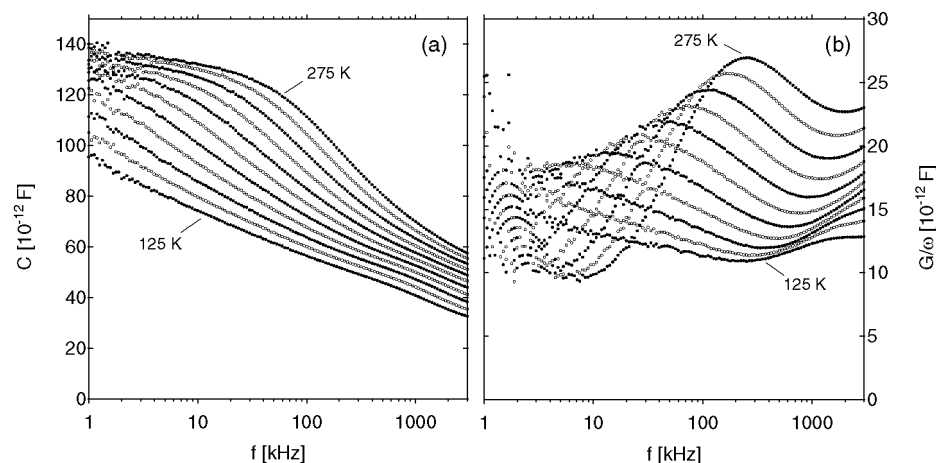


Fig. 2. a) Fit of G/ω curve at room temperature (RT) and zero bias (full line). Dashed lines show the contributions of the different RC series circuits (P_1 to P_5) and of R_p ; b) complete results of the fitting procedure for G/ω at temperatures between 275 and 215 K. The inset in b) shows a schematic representation of the equivalent electrical circuit used in the fit procedure for $T > 245$ K

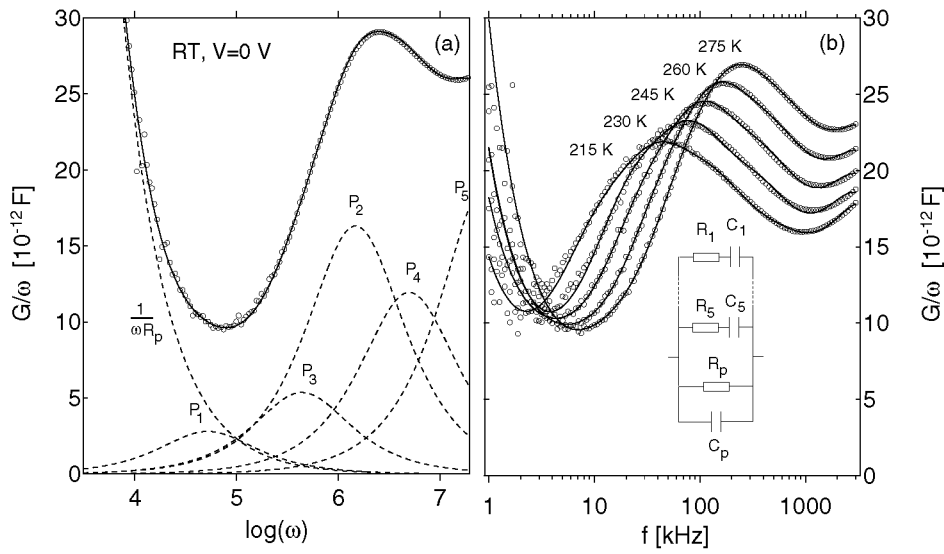


Fig. 3. Experimental (circles and squares) and fitted (full line) curves of a) $Re(Z)$ and $-Im(Z)$, b) C and G/ω at room temperature (RT) and zero applied voltage, where Z is the impedance

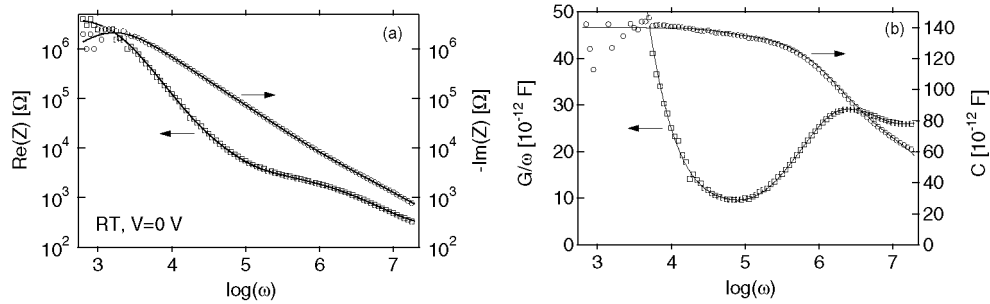
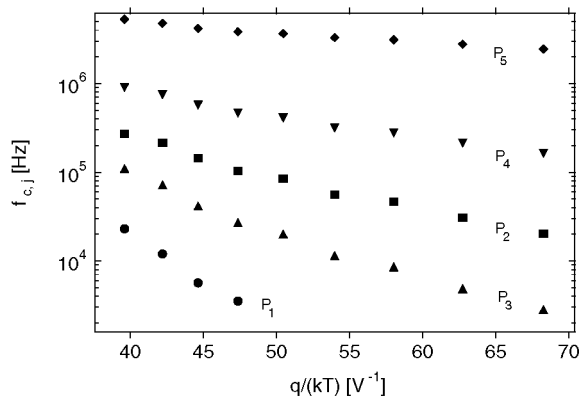


Fig. 4. Arrhenius plot of the cutoff frequencies $f_{c,j} = 1/(2\pi R_j C_j)$ of the five RC series circuits



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